

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	36	(DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "amplifier"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:15
L3	45465	((370/251) or (370/250) or (370/356) or (370/398) or (370/422) or (370/489) or (370/321) or (370/326) or (370/337) or (370/395) or (370/397) or (370/399) or (370/401) or (370/404) or (370/409) or (370/396) or (370/425) or (370/442) or (370/352) or (370/486) or (370/487) or (370/493) or (370/496) or (370/503) or (370/509) or (370/522) or (370/537) or (370/538) or (370/540) or (370/336) or (370/347) or (370/470) or (370/498) or (370/524) or (370/433) or (370/468) or (370/494) or (370/495) or (370/248) or (370/484) or (370/487) or (370/229) or (370/477) or (379/377) or (379/382) or (379/382) or (379/383) or (379/384) or (379/380) or (379/398) or (379/399.01) or (379/413) or (379/412) or (379/413.02) or (379/414) or (379/26.02) or (379/373) or (379/418) or (379/252) or (379/399) or (379/1) or (379/2) or (379/26) or (379/27) or (379/28) or (379/29) or (379/32) or (379/34) or (379/6) or (379/30) or (359/109) or (359/125) or (359/115) or (359/118) or (359/135) or (375/222) or (375/224) or (375/225) or (375/228) or (375/202) or (341/118) or (341/120) or (341/143) or (324/158.1) or (324/607) or (324/620) or (324/621) or (324/622) or (324/623) or (324/624)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:29

L4	8	L3 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "amplifier"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L5	0	L3 and (DSP or "digital signal processor") and "line card" and "transmit channel" and "received channel" and "combined channel" and "self-test"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L6	0	L3 and (DSP or "digital signal processor") and "line card" and "transmit channel" and "received channel" and "combined channel" and "self-test" and "switch"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L9	35	L3 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L10	0	L3 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "transmit channel"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L11	0	L3 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier" and ("analog to digital converter" or "digital to analog converter") and (@prad<"20011218" or @rlad<"20011218")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L12	2	L3 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier" and (@prad<"20011218" or @rlad<"20011218")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L13	7	L3 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:32

L14	50988	((370/251) or (370/250) or (370/356) or (370/398) or (370/422) or (370/489) or (370/321) or (370/326) or (370/337) or (370/395) or (370/397) or (370/399) or (370/401) or (370/404) or (370/409) or (370/396) or (370/425) or (370/442) or (370/352) or (370/486) or (370/487) or (370/493) or (370/496) or (370/503) or (370/509) or (370/522) or (370/537) or (370/538) or (370/540) or (370/336) or (370/347) or (370/470) or (370/498) or (370/524) or (370/433) or (370/468) or (370/494) or (370/495) or (370/248) or (370/484) or (370/487) or (370/229) or (370/477) or (379/377) or (379/382) or (379/382) or (379/383) or (379/384) or (379/380) or (379/398) or (379/399.01) or (379/413) or (379/412) or (379/413.02) or (379/414) or (379/26.02) or (379/373) or (379/418) or (379/252) or (379/399) or (379/1) or (379/2) or (379/26) or (379/27) or (379/28) or (379/29) or (379/32) or (379/34) or (379/6) or (379/30) or (359/109) or (359/125) or (359/115) or (359/118) or (359/135) or (375/222) or (375/224) or (375/225) or (375/228) or (375/202) or (341/118) or (341/120) or (341/143) or (324/158.1) or (324/607) or (324/620) or (324/621) or (324/622) or (324/623) or (324/624) or (702/108) or (702/117) or (702/118) or (702/123) or (702/124) or (702/182) or (702/183) or (702/185) or (702/188)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:30
L15	9	L14 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "amplifier"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31

L16	0	L14 and (DSP or "digital signal processor") and "line card" and "transmit channel" and "received channel" and "combined channel" and "self-test"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L17	0	L14 and (DSP or "digital signal processor") and "line card" and "transmit channel" and "received channel" and "combined channel" and "self-test" and "switch"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L18	37	L14 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L19	0	L14 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "transmit channel"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L20	0	L14 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier" and ("analog to digital converter" or "digital to analog converter") and (@prad<"20011218" or @rlad<"20011218")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L21	2	L14 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier" and (@prad<"20011218" or @rlad<"20011218")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:31
L22	8	L14 and (DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:32
S21 3	0	(DSP or "digital signal processor") and "line card" and "transmit channel" and "received channel" and "combined channel" and "self-test"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:15
S21 4	0	(DSP or "digital signal processor") and "line card" and "transmit channel" and "received channel" and "combined channel" and "self-test" and "switch"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:15

S21 5	191	(DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:16
S21 6	0	(DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "transmit channel"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:16
S21 7	35	(DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:17
S21 8	24	(DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier" and (@prad<"20011218" or @rlad<"20011218")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:16
S21 9	0	(DSP or "digital signal processor") and "line card" and ("test signal" or "self test") and "switch" and "amplifier" and ("analog to digital converter" or "digital to analog converter") and (@prad<"20011218" or @rlad<"20011218")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/27 11:16